

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10669095			
<b>Filing Date:</b>	23-Sep-2003			
<b>Title of Invention:</b>	Logic multiprocessor for FPGA implementation			
<b>First Named Inventor:</b>	Michael R. Butts			
<b>Filer:</b>	Hope E. Melville/Lois Kimmerle			
<b>Attorney Docket Number:</b>	706316-1224			
Filed as Large Entity				
<b>Utility      Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	120	120

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	790	790
Total in USD (\$)				910